

UNITED STATES PATENT AND TRADEMARK OFFICE

Sector #
#3

Applicants: Kalnitsky, et al.

Atty Docket: 60305-307201

Application No: 09/882,538

Group Art Unit: 2811

Filed: June 15, 2001

Title: **METHOD FOR FORMING A SUPER SELF-ALIGNED HETERO-JUNCTION BIPOLAR TRANSISTOR**

Box Missing Parts
Commissioner of Patents
Washington, D.C. 20231

TRANSMITTAL FOR RESPONSE TO FILE MISSING PARTS

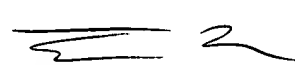
Enclosed herewith for filing in the above-identified application are the following:

- ☒ Copy of Notice to File Missing Parts of NonProvisional Application (Filing Date Granted);
- ☒ Executed Declaration, Power of Attorney and Petition;
- ☒ Check No.: 126739 in the amount of \$840.00 for payment of the required fee (as payment for Missing Parts Fee and Filing Fee); and
- ☒ Postcard for date-stamped return as confirmation of receipt of these items.

Total fees due in this application amount to \$840.00. The Commissioner is further authorized to charge any required additional fees, or credit any overpayment, to deposit account 02-3964.

Respectfully submitted,

DATE: October 8, 2001



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CERTIFICATE OF MAILING (37CFR 1.8 (a))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited on October 8, 2001 with the U.S. Postal Service as first class mail in an envelope addressed to: Commissioner of Patents, Washington, D.C. 20231.

Date: October 8, 2001



Sheena Hicks

10/16/2001 #DELETED 60305023 023964 09882538
01 FC:105 130.00 OP
02 FC:101 710.00 OP
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